

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Leonard Forbes et al.

Application No.: Not Yet Assigned

Art Unit: Not Yet Assigned

Filed: Concurrently Herewith

Examiner: Not Yet Assigned

For: **TECHNIQUE TO MITIGATE SHORT
CHANNEL EFFECTS WITH VERTICAL
GATE TRANSISTOR WITH DIFFERENT
GATE MATERIALS**

PRELIMINARY AMENDMENT

MS Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Prior to examination on the merits, please amend the above-identified U.S. patent application as follows:

Amendments to the Specification are shown beginning on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims, which begins on page 3 of this paper.

Please consider the **Remarks**, which begin on page 9 of this paper.